

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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First Named Inventor	Aarti GUPTA
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Examiner Name	Juan C. Ochoa
Attorney Docket Number	A8625

**U.S. PATENTS**

Examiner Initials*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

**U.S. PATENT APPLICATION PUBLICATIONS**

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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	T <sup>5</sup>
/JO/	1	LINTAO ZHANG, et al.; Validating SAT Solvers Using an Independent Resolution-Based Chekcer: Practical Implementations and Other Applications; Design, Automation, and Test in Europe, Proceedings of the conference on Design, Automation, and Test in Europe Volume 1; 2003, ISBN~ISSN:1530-1591, 0-7695-1870-2, Pages 6, IEEE Computer Society, Washington, DC, USA.	
/JO/	2	KENNETH L. MCMILLAN et al.; Automatic Abstraction without Counterexamples; Cadence Design Systems; H. Garavel and J. Hatcliff (Eds.): TACAS 2003; LNCS 2619; 2003; pp 2-17;	

**EXAMINER SIGNATURE**

Examiner Signature	/Juan Ochoa/	Date Considered	08/26/2008
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